

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application:	)	For:	CACHED MEMORY SYSTEM
Sih, et al.	)		AND CACHE CONTROLLER
	)		FOR EMBEDDED DIGITAL
Application No.: 10/807,648	)		SIGNAL PROCESSOR
	)		
Filed: March 24, 2004	)	Examiner:	Song, Jasmine
	)		
Confirmation No.: 3901	)	Group No.:	2188
	)		
	)	Docket No.:	030459

Mailstop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

## AMENDMENT UNDER 37 C.F.R. § 1.111

Sir:

In response to the non-Final Office Action of October 23, 2007, please amend the above-identified application as follows:

**Amendments to the Drawings** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the listing of claims which begins on page 3 of this paper; and

**Remarks/Arguments** begin on page 10 of this paper.

**CERTIFICATE OF MAILING/TRANSMISSION (37 CFR 1.8(a))**

I hereby certify that this correspondence is, on the date shown below, being:

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